Serial No.: 10/616,796 Filing Date: 7/10/2003 Title: RADIO RECEIVER

Title: RADIO RECEIVER

# AMENDMENTS TO THE CLAIMS

The following listing of claims will replace all prior versions and listings of claims in the application.

## **Listing of Claims**

(currently amended) A radio receiver, comprising:

a front-end circuit operable to receive a plurality of radio signals transmitted across a frequency band and generate an analog signal corresponding to a plurality of channels within said frequency band;

an analog to digital converter coupled to said front-end circuit, said analog to digital converter operable to receive and convert said analog signal to a digital signal; and

a digital processing system coupled to said analog to digital converter, said digital processing system operable to receive said digital signal and generate at least one output signal corresponding to at least one of said plurality of channels within said frequency band, said digital processing system comprising:

a digital down converter operable to select said at least one of said channels within said frequency band; and

a digital signal processor operable to extract information from said at least one of said channels and generate said at least one output signal, wherein said digital signal processor controls said digital down converter.

(original) The radio receiver of claim 1, wherein said digital processing system generates a single output signal comprising a time-domain multiplexed serial data link.

PAGE 3

Serial No.: 10/616,796 Filing Date: 7/10/2003

Title: RADIO RECEIVER

3. (original) The radio receiver of claim 2, further comprising a controller coupled to said digital processing system, said controller operable to receive said time-domain multiplexed serial

data link and generate a plurality of signals for transmission to a plurality of end devices.

4. (original) The radio receiver of claim 1, wherein said digital processing system generates a plurality of output signals comprising a plurality of signals for transmission to a plurality of

end devices.

5. (original) The radio receiver of claim 1, wherein said front-end circuit comprises an

antenna circuit operable to receive said radio signals.

6. (original) The radio receiver of claim 5, wherein said front-end receiver further

comprises an amplifier circuit operable to amplify said received radio signals.

7. (original) The radio receiver of claim 5, wherein said front-end circuit further comprises

a filter circuit operable to filter said received radio signals.

8. (previously presented) The radio receiver of claim 7, wherein said filter circuit

comprises a filter selected from the group consisting of high-pass filter, low-pass filter, band-

pass filter, notch filter, and combinations thereof.

9. (original) The radio receiver of claim 5, wherein said front-end circuit further comprises

an intermediate frequency mixing circuit operable to translate said received radio signals to an intermediate frequency band.

10. (cancelled) Serial No.: 10/616,796 Filing Date: 7/10/2003

Title: RADIO RECEIVER

11. (currently amended) The radio receiver of claim 1/40, wherein said digital down converter selects said at least one of said channels according to configurable channel selection parameters.

- 12. (original) The radio receiver of claim 11, wherein said configurable channel selection parameters are software configurable.
- 13. (previously presented) The radio receiver of claim 11, wherein said configurable channel selection parameters are selected from the group consisting of channel frequency, channel bandwidth, and combinations thereof.
- 14. (currently amended) The radio receiver of claim 1/10, wherein said digital signal processor extracts said information from said at least one of said selected channels according to configurable channel decoding parameters.
- (original) The radio receiver of claim 14, wherein said configurable channel decoding parameters are software configurable.
- 16. (previously presented) The radio receiver of claim 14, wherein said configurable channel decoding parameters are selected from the group consisting of channel frequency, channel modulation scheme, channel bandwidth, channel information format, and combinations thereof.
- 17. (cancelled)
- 18. (currently amended) A radio receiver, comprising:

at least one front-end circuit group comprising a plurality of front-end circuits, wherein each of said front-end circuits is operable to receive a plurality of radio signals transmitted across a frequency band and generate an analog signal corresponding to a plurality of channels within said frequency band;

Attorney Docket No. H0004400-5542/400.444US01

at least one analog to digital converter coupled to said at least one front-end circuit group, said analog to digital converter operable to receive said analog signal signals from said front-end circuits and convert said analog signal signals to a digital signal; and

a digital processing system coupled to said at least one analog to digital converter, said digital processing system operable to receive said digital signal from said analog to digital converter and generate at least one output signal corresponding to at least one of said channels within said frequency band of at least one of said front-end circuits;

wherein said radio signals received by any one of said front-end circuits are within a different frequency band than said radio signals received by the other of said front-end circuits.

- 19. (original) The radio receiver of claim 18, wherein said digital processing system is operable to generate a plurality of output signals, wherein each of said output signals corresponds to at least one of said channels within said frequency band of at least one of said front-end circuits.
- (cancelled)
- 21. (original) The radio receiver of claim 18, wherein each of said front-end circuits comprises an antenna circuit operable to receive said radio signals.
- 22. (original) The radio receiver of claim 21, wherein at least one of said front-end circuits further comprises an amplifier circuit operable to amplify said received radio signals.
- 23. (original) The radio receiver of claim 21, wherein at least one of said front-end circuits further comprises a filter circuit operable to filter said received radio signals.

Serial No.: 10/616,796 Filing Date: 7/10/2003

Attorney Docket No. H0004400-5542/400,444US01

PAGE 6

Title: RADIO RECEIVER

24. (original) The radio receiver of claim 21, wherein at least one of said front-end circuits further comprises an intermediate frequency mixing circuit operable to translate said received

radio signals to an intermediate frequency band.

25. (original) The radio receiver of claim 18, wherein said digital processing system

comprises:

a digital down converter operable to select said at least one of said channels

within said frequency band of at least one of said front-end circuits; and

a digital signal processor operable to extract information from said at least one of

said selected channels and generate said at least one output signal.

26. (original) The radio receiver of claim 25, wherein said digital down converter selects

said at least one of said channels according to software configurable channel selection

parameters.

27. (original) The radio receiver of claim 25, wherein said digital signal processor extracts

said information from said at least one of said selected channels according to software

configurable channel decoding parameters.

 (original) The radio receiver of claim 18, comprising a plurality of front-end circuit groups and a plurality of corresponding analog to digital converters, wherein said digital

processing system is operable to receive a plurality of digital signals from said analog to digital

converters and generate at least one output signal corresponding to at least one of said channels

within said frequency band of at least one of said front-end circuits of at least one of said front-

end circuit groups.

29. (currently amended) A radio receiver, comprising:

a plurality of front-end circuits each of which comprises an antenna circuit

operable to receive a plurality of radio signals transmitted across a frequency band,

Serial No.: 10/616,796 Filing Date: 7/10/2003 Title: RADIO RECEIVE

Title: RADIO RECEIVER

wherein each of said <u>front-end front end</u> circuits is operable to generate an analog signal corresponding to a plurality of channels within said frequency band;

a plurality of analog to digital converters each of which is coupled to at least one of said front-end circuits, wherein each of said analog to digital converters is operable to receive said analog signal from said at least one of said front-end circuits and convert said analog signal to a digital signal; and

a digital processing system coupled to each of said analog to digital converters, said digital processing system operable to receive said digital signals from said analog to digital converters and generate at least one output signal corresponding to at least one of said channels within said frequency band of at least one of said front-end circuits;

wherein said radio signals received by any one of said front-end circuits are within a different frequency band than said radio signals received by the other of said front-end circuits.

- 30. (original) The radio receiver of claim 29, wherein at least one of said front-end circuits further comprises an amplifier circuit operable to amplify said received radio signals.
- 31. (original) The radio receiver of claim 29, wherein at least one of said front-end circuits further comprises a filter circuit operable to filter said received radio signals.
- 32. (original) The radio receiver of claim 29, wherein at least one of said front-end circuits further comprises an intermediate frequency mixing circuit operable to translate said received radio signals to an intermediate frequency band.
- 33. (original) The radio receiver of claim 29, wherein at least one of said analog to digital converters is operable to receive analog signals from a plurality of front-end circuits and convert said analog signals to a digital signal.
- 34. (currently amended) A method of receiving radio signals, comprising:

Serial No.: 10/616,796 Filing Date: 7/10/2003 Title: RADIO RECEIVER

Attorney Docket No. H0004400-5542/400.444US01

receiving <u>at a plurality of front-end circuits</u> a plurality of radio signals transmitted across a frequency band, <u>wherein said radio signals received by any one of said front-end circuits are within a different frequency band than said radio signals received by the other of said front-end circuits;</u>

generating an analog signal from said received radio signals, said analog signal comprising a plurality of channels within said frequency band;

converting said analog signal to a digital signal to thereby digitize said plurality of channels within said frequency band; and

generating at least one output signal corresponding to at least one of said digitized channels within said frequency band.

- (original) The method of claim 34, further comprising amplifying said received radio signals.
- (original) The method of claim 34, further comprising filtering said received radio signals.
- (original) The method of claim 34, further comprising mixing said received radio signals to an intermediate frequency band.
- 38. (original) The method of claim 34, wherein said output signal is generated by:

applying software configurable channel selection parameters to said digital signal to select at least one of said digitized channels within said frequency band;

extracting information from said at least one of said selected digitized channels according to software configurable channel decoding parameters; and conveying said extracted information within said output signal.

 (original) The method of claim 38, wherein said output signal comprises a time-domain multiplexed serial data link.

Serial No.: 10/616,796 Filing Date: 7/10/2003 Title: RADIO RECEIVER

Attorney Docket No. H0004400-5542/400,444US01

40. (original) The method of claim 39, further comprising generating a plurality of signals from said time-domain multiplexed serial data link for transmission to a plurality of end devices.

- 41. (original) The method of claim 34, wherein a plurality of output signals are generated each of which corresponds to at least one of said digitized channels within said frequency band.
- 42. (original) The method of claim 41, wherein said plurality of output signals comprise a plurality of signals for transmission to a plurality of end devices.
- 43. (currently amended) A radio receiver, comprising:

a plurality of front-end circuits each of which is operable to receive a plurality of radio signals transmitted across a frequency band and generate analog signals that are combined into a composite analog signal corresponding to a plurality of channels within said frequency band;

a single analog to digital converter operable to receive the composite analog signal and convert the composite analog signal to a single digital signal;

a digital processing system operable to receive said digital signal from said analog to digital converter and generate at least one output signal corresponding to at least one of said channels within said frequency band.

means-for-receiving a plurality of radio signals transmitted across a frequency band and generating an analog signal corresponding to a plurality of channels within said frequency band;

means for converting said analog signal to a digital signal; and

means for generating at least one output signal from said digital signal, said output signal corresponding to at least one of said channels within said frequency band.

44. (currently amended) The radio receiver of claim 43, wherein said output signal comprises a time-domain multiplexed serial data link.

PAGE 10

Attorney Docket No. H0004400-5542/400.444US01

Serial No.: 10/616,796 Filing Date: 7/10/2003

45.

Title: RADIO RECEIVER

AMENDMENT AND RESPONSE

(currently amended) The radio receiver of claim 44, further comprising means for generating a plurality of signals from said time domain multiplexed serial data link for

transmission to a plurality of end devices a controller coupled to said digital processing system, said controller operable to receive said time-domain multiplexed serial data link and generate a

plurality of signals for transmission to a plurality of end devices.

46. (currently amended) An aviation-band radio receiver, comprising:

a front-end circuit operable to receive a plurality of radio signals transmitted

across a frequency band and generate an analog signal corresponding to a plurality of

channels within said frequency band, wherein said frequency band comprises aviation-

band radio signals;

an analog to digital converter coupled to said front-end circuit, said analog to

digital converter operable to receive and convert said analog signal to a digital signal;

and

a digital processing system coupled to said analog to digital converter, said digital

processing system operable to receive said digital signal and generate at least one output

signal corresponding to at least one of said plurality of channels within said frequency

band, said digital processing system comprising:

a digital down converter operable to select said at least one of said

channels within said frequency band; and

a digital signal processor operable to extract information from said at least one of said channels and generate said at least one output signal, wherein said

digital signal processor controls said digital down converter.

47 (previously presented) The aviation radio receiver of claim 46, wherein said aviation-

band radio signals comprise aviation navigation radio signals.

Serial No.: 10/616,796

Attorney Docket No. H0004400-5542/400.444US01

PAGE 11

Filing Date: 7/10/2003

Title: RADIO RECEIVER

48. (previously presented) The aviation radio of claim 46, wherein said aviation-band radio

signals comprise aviation communication radio signals.

(previously presented) The aviation radio of claim 46, wherein said aviation-band radio

signals comprise aviation navigation and aviation communication radio signals.

50. (previously presented) The aviation radio receiver of claim 49, wherein said digital

processing system generates a single output signal comprising a time-domain multiplexed serial

data link.

49.

51. (previously presented) The aviation radio receiver of claim 46, further comprising a

controller coupled to said digital processing system, said controller operable to receive said time-

domain multiplexed serial data link and generate a plurality of signals for transmission to a

plurality of end devices.

52. (previously presented) The aviation radio receiver of claim 46, wherein said digital

processing system generates a plurality of output signals comprising a plurality of signals for

transmission to a plurality of end devices.

53. (currently amended) An aviation radio receiver comprising:

at least one front-end circuit group comprising a plurality of front-end circuits,

wherein each of said front-end circuits comprises an antenna circuit operable to receive radio signals and an amplifier circuit operable to amplify radio signals, said front-end

circuits operable to receive a plurality of radio signals transmitted across a frequency

band and generate an analog signal corresponding to a plurality of channels within said

frequency band, wherein said radio signals comprise aviation navigation and aviation

communication radio signals;

at least one analog to digital converter coupled to said at least one front-end

circuit group, said analog to digital converter operable to receive said analog signal

PAGE 12

AMENDMENT AND RESPONSE

Attorney Docket No. H0004400-5542/400.444US01

Serial No.: 10/616,796 Filing Date: 7/10/2003

Title: RADIO RECEIVER

signals from said front-end circuits and convert said analog signal signals to a digital signal; and

a digital processing system coupled to said at least one analog to digital converter, said digital processing system operable to receive said digital signal from said analog to digital converter and generate at least one output signal corresponding to at least one of said channels within said frequency band of at least one of said front-end circuits;

wherein said radio signals received by any one of said front-end circuits are within a different frequency band than said radio signals received by the other of said front-end circuits.

- 54. (previously presented) The aviation radio receiver of claim 53, wherein said digital processing system is operable to generate a plurality of output signals, wherein each of said output signals corresponds to at least one of said channels within said frequency band of at least one of said front-end circuits.
- 55. (cancelled)
- 56. (previously presented) The radio receiver of claim 54, wherein said digital processing system comprises:
  - a digital down converter operable to select said at least one of said channels within said frequency band of at least one of said front-end circuits; and
  - a digital signal processor operable to extract information from said at least one of said selected channels and generate said at least one output signal.
- 57. (currently amended) A method of receiving aviation-band radio signals, comprising:
  - receiving at a plurality of front-end circuits a plurality of radio signals transmitted across an aviation frequency band, said radio signals comprising aviation navigation and aviation communication radio signals, wherein said radio signals received by any one of

Serial No.: 10/616,796 Filing Date: 7/10/2003 Title: RADIO RECEIVER

Attorney Docket No. H0004400-5542/400,444US01

said front-end circuits are within a different frequency band than said radio signals received by the other of said front-end circuits;

generating an analog signal from said received radio signals, said analog signal comprising a plurality of channels within said frequency band;

converting said analog signal to a digital signal to thereby digitize said plurality of channels within said frequency band; and

generating at least one output signal corresponding to at least one of said digitized channels within said frequency band.

- 58. (previously presented) The method of claim 57, further comprising mixing said received radio signals to an intermediate frequency band.
- 59. (previously presented) The method of claim 58, wherein said output signal is generated by:

applying software configurable channel selection parameters to said digital signal to select at least one of said digitized channels within said frequency band;

extracting information from said at least one of said selected digitized channels according to software configurable channel decoding parameters; and conveying said extracted information within said output signal.